

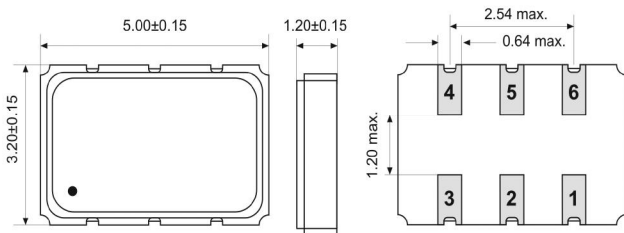
LVDS Clock Oscillator

SMD-version

+2.5 / +3.3V

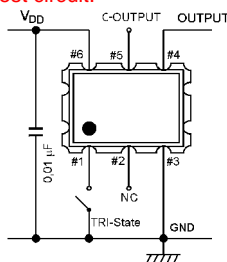
model	KXO-V66
frequency range	40.0 ~ 600.0 MHz
frequency stability incl. temperature stability, input voltage and load stability, aging. at -20° ~ +70°C at -40° ~ +85°C	±25 ppm ~ ±100 ppm ±25 ppm ~ ±100 ppm
operating temperature	standard -20° ~ +70°C available -40° ~ +85°C (=KXO-V66T)
storage temperature	-55° ~ +125°C
symmetry	50% ± 5% at ½ Output level
rise & fall time (Tr) max.	300 ps typ. 600 ps max. (20% ~ 80% of waveform)
rise & fall time (Tf) max.	300 ps typ. 600 ps max. (80% ~ 20% of waveform)
LVDS offset output voltage	1.125V ~ 1.375V
"O" level max.	0.9V ~ 1.1V
"1" level min.	1.4V ~ 1.6V
input voltage V _{DD}	standard + 3.3V ± 5% available +2.5V ±5%
input current	80 mA
output load	100 Ohm
start up time max.	10 ms
tristate function	yes
disable delay time	200 ns max.
enable delay time	4 ms max.
phase jitter (12kHz ~ 20MHz)	RMS: 1ps max.
typical phase noise	-70 dBc/Hz at 10 Hz -105 dBc/Hz at 100 Hz -130 dBc/Hz at 1 kHz -145 dBc/Hz at 10 kHz -145 dBc/Hz at 100 kHz -145 dBc/Hz at 1 MHz
contents of reel	1000 pcs.
part no.	12.xxxxx

Dimensions (mm):

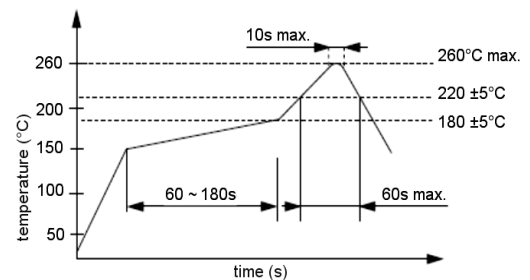


PIN	CONNECTION
1	Tri-state*
2	NC
3	GND
4	Output
5	C-Output
6	V _{DD}

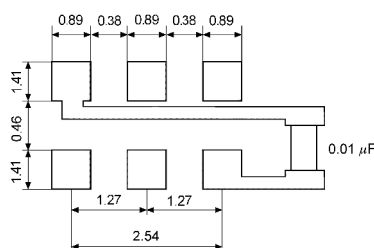
Test circuit:



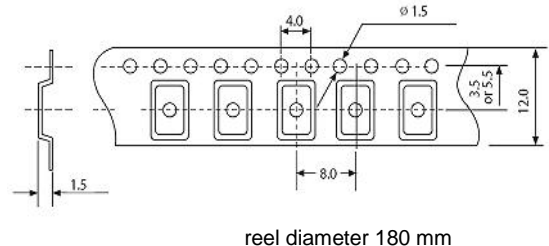
Reflow soldering condition:



Suggested soldering pad:



Tape specification:



note:
A capacitor of value 0,01μF and 10μF between V_{DD} and GND is recommended.