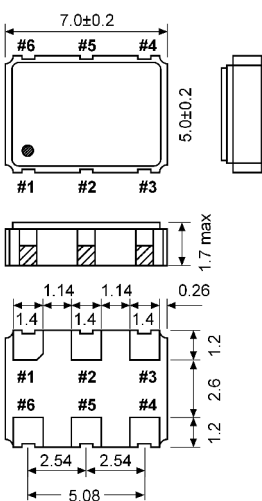


LVDS Clock Oscillator

SMD-version +2,5V / +3,3V

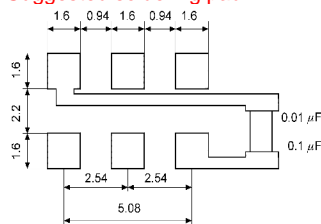
model	KXO-V65
frequency range	19,440 ~ 700,0 MHz
frequency stability at -20° ~ +70°C at -40° ~ +85°C	± 50 ppm ± 100 ppm
operating temperature	standard -20° ~ +70°C available -40° ~ +85°C (= KXO-V65T)
storage temperature	-55° ~ +125°C
symmetry	50% ±5% at ½ V _{DD} level
rise and fall time (Tr) max.	300ps typ. 600ps max. (20% ~ 80% of waveform)
rise and fall time (Tf) max.	300ps typ. 600ps max. (80% ~ 20% of waveform)
LVDS offset output voltage	1,125V ~ 1,375V
"0" level max.	0,9V ~ 1,1V
"1" level min.	1,43V ~ 1,6V
input voltage V _{DD}	standard +3,3V ±5% available +2,5V ±5%
input current max.	80 mA
output load	100 Ohm
start up time max.	10 ms
tristate function	yes
disable delay time	200 ns max.
enable delay time	4 ms max.
typical phase noise	-70 dBc/Hz at 10 Hz -105 dBc/Hz at 100 Hz -130 dBc/Hz at 1 kHz -145 dBc/Hz at 10 kHz -145 dBc/Hz at 100 kHz -145 dBc/Hz at 1 MHz
phase jitter (12kHz ~ 20MHz)	RMS: 19.440 ~ 212,5 MHz 1ps max. < 700,0 MHz 4ps max.
contents of reel	1000 pcs.
part no.	12.xxxxx

Dimensions (mm):

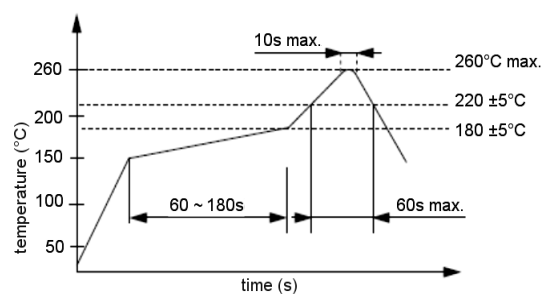


PIN	CONNECTION
1	Tri-state
2	NC
3	GND
4	Output
5	C-Output
6	V _{DD}

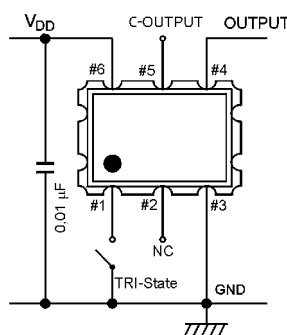
Suggested soldering pad:



Reflow soldering condition:



Test circuit:



Tape specification:

