

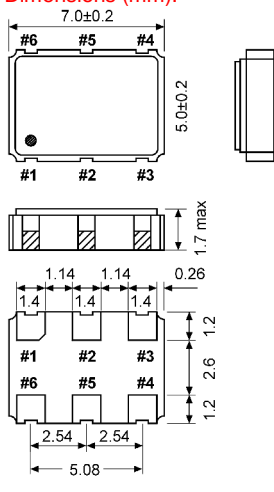
# PECL Positive Emitter Coupled Oscillator

# SMD-version

model	KXO-67
frequency range	50,0 – 212,50 MHz
frequency stability over all conditions	±100 ppm = KXO-67A ± 50 ppm = KXO-67B ± 25 ppm = KXO-67D
storage temp.range	-50°C ~ +125°C
operating temp.range	standard -20°C ~ +70°C available -40°C ~ +85°C
symmetry	40% ~ 60% (at crossing point)
rise and fall time (max.)	0,8 ns (20% ~ 80% of amplitude)
start up time (max.)	10 ms
"0" level max.	V <sub>DD</sub> to +1,63V
"1" level min.	V <sub>DD</sub> to +1,02V
disable delay time max.	100 ns
enable delay time max.	10 ms
input voltage (V <sub>DD</sub> )	+3,3V DC ±5%
supply voltage	-0,5V to +7,0V
input current (Pin#1=Open or VIH)	90 mA max.
load	50 Ohm (V <sub>DD</sub> to +2,0V)
stand-by control voltage	VIH: +0,7V <sub>DD</sub> min. VIL: +0,3V <sub>DD</sub> max.*
stand-by current (Pin#1=VIL)	100 µA max.
phase jitter (12 kHz to 20 MHz band)	1 ps RMS max.
aging at 25°C (first year)	± 5ppm max.
typical phase noise	-70 dBc/Hz at 10 Hz -105 dBc/Hz at 100 Hz -130 dBc/Hz at 1 kHz -145 dBc/Hz at 10 kHz -145 dBc/Hz at 100 kHz -145 dBc/Hz at 1 MHz
contents of reel	1000 pcs.
part no.	12.xxxxx

\* Internal crystal oscillation to be halted (Pin#1=VIL).

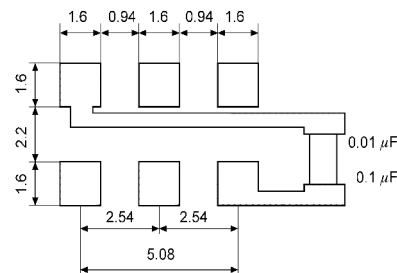
### Dimensions (mm):



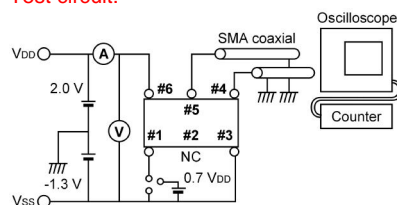
PIN	CONNECTION
1	Tri-state*
2	NC
3	GND
4	Output
5	C-Output
6	V <sub>DD</sub>

\*enable high

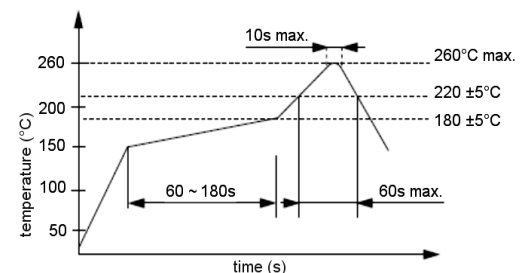
### Suggested soldering pad:



### Test circuit:



### Reflow soldering condition:



### Tape specification:

