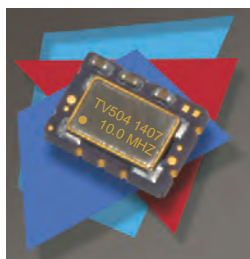


Description:

Connor-Winfield's Txxx and TVxxx series are 5x7mm TCXO and VCTCXO products with exceptional frequency stability and low phase noise. Through the use of analog temperature compensation, these products are capable of holding Stratum 3 level temperature stabilities of ± 0.28 ppm over the commercial and industrial temperature ranges. Available in 4-pad or 10-pad surface mount footprints.

These products are designed for such applications as IEEE 1588 PTP and Synchronous Ethernet.

All models will meet ± 4.6 ppm accuracies for twenty years



Features:

- Frequency Stabilities Available:
 - ± 0.28 ppm (6.4 to 50 MHz) 
 - ± 0.50 ppm (6.4 to 50 MHz)
 - ± 1.00 ppm or ± 2.00 ppm (6.4 to 54 MHz)
- Temperature Ranges Available:
 - 0 to 85°C, 0 to 70°C, -40 to 85°C or -20 to 70°C Packages Available:
- T - Series: 5 x 7mm - 10 Pad
- TV - Series: 5 x 7mm - 4 Pad
- 3.3 Vdc Operation
- Output Logic: LVCMOS or Clipped Sinewave
- Fixed Frequency - TCXO
- Voltage Controlled - VCTCXO
- Low Jitter <0.50 ps RMS
- Low Phase Noise
- Tri-State Enable/Disable: (T Model Series Only)
- Tape and Reel Packaging
- RoHS Compliant / Lead Free 

Applications:

- IEEE 1588 Applications
- Synchronous Ethernet slave clocks, ITU-T G.8262 EEC options 1 & 2
- Compliant to Stratum 3, GR-1244-CORE & GR-253-CORE
- Wireless Communications
- Small Cells
- Test and Measurement
- GPS

Standard Frequencies Available *

* 6.4, 9.72, 10, 10.24, 12.5, 12.8, 13.5, 19.2, 19.44, 20, 20.48, 25, 27, 38.88, 40 MHz
Available frequencies from the factory for small quantity orders or quick delivery.
Additional frequencies are available.

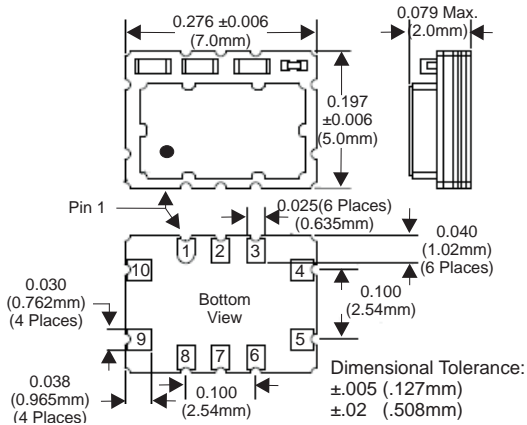
** Not all
Models
available at
Digi-Key

Ordering Information

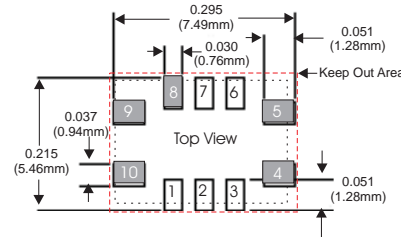
TV	5	0	4	- 010.0M
Type / Package TCXO / VCTCXO Series T = 5.0x7.0 mm 10 Pads TV = 5.0x7.0 mm 4 Pads	Temperature Range 3 = 0 to 85 °C 5 = 0 to 70°C 6 = -40 to 85°C 7 = -20 to 70°C	Frequency Stability 0 = ± 0.28 ppm 1 = ± 0.50 ppm 2 = ± 1.00 ppm 3 = ± 2.00 ppm	Features 2 = TCXO, LVCMOS, 3.3 Vdc 3 = TCXO, Clipped Sinewave, 3.3 Vdc 4 = VCTCXO, LVCMOS, 3.3 Vdc 5 = VCTCXO, Clipped Sinewave, 3.3 Vdc	Output Frequency Frequency Format -xxx.xM Min -xxx.xxxxxM Max *Amount of numbers after the decimal point. M = MHz



T Series Package Outline



T Series Suggested Pad Layout

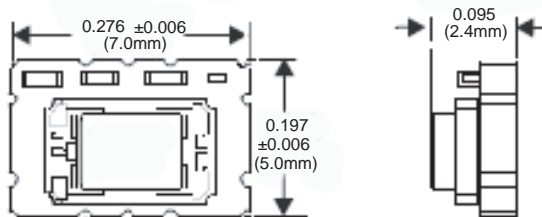


* Do not route any traces in the keep out area. It is recommended the next layer under the keep out area is to be ground plane.

T Series Pad Connections

- 1: Do Not Connect
- 2: Do Not Connect
- 3: Do Not Connect
- 4: Ground
- 5: Output
- 6: Do Not Connect
- 7: Do Not Connect
- 8: Enable / Disable (OE)
- 9: Supply Voltage (Vcc)
- 10: VCTCXO: Control Voltage (Vc)
TCXO: N/C

T Series Alternate Package Outline



Alternate package applies to some frequencies where a smaller crystal size is used.

The differences are the top view crystal size, and the overall height.

Bottom view, suggested pad layout, and pad connections all remain the same as above.

Marking Information

The following are examples of possible marking configurations

