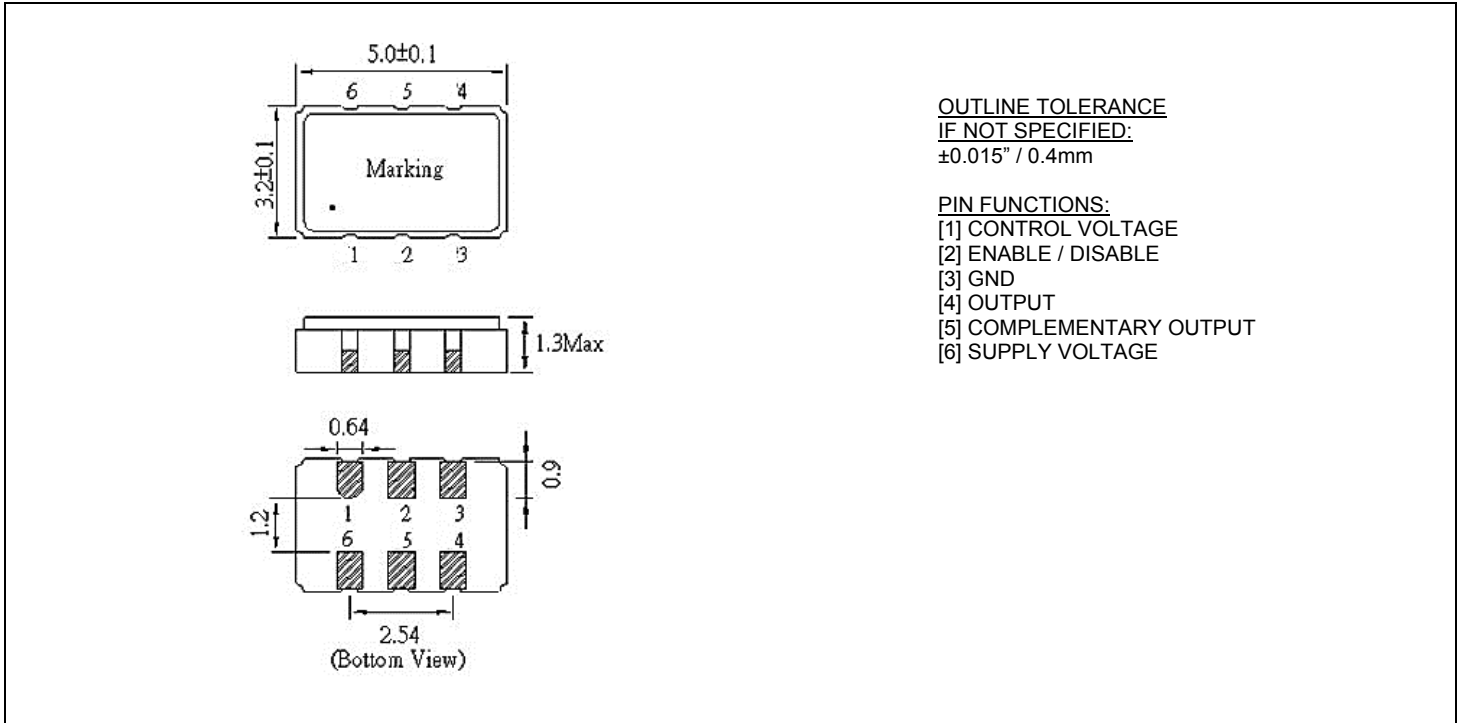




■ ELECTRICAL SPECIFICATION

PARAMETER		VALUE
Frequency Range (F ₀)		50.000 ~ 200.000 MHz
Supply Voltage (V _S)		3.3 ± 10% VDC
Oscillation Mode		Fundamental
Control Voltage Range (V _{CC})		1.65 ± 1.65 VDC
Input Current		80 mA max
Frequency Stability		±20 ppm, ±25 ppm, ±50 ppm, ±100 ppm
Frequency Adjustment Range		±50 ppm, ±100 ppm min
Operating Temperature Range		-10 ~ +70°C -40 ~ +85°C
Storage Temperature Range		-55 ~ +125°C
Output LVPECL	Symmetry at 50% V _S	40% ~ 60% Standard 45% ~ 55% Tight
	Rise / Fall Time	1.0 ns max
	Logic "0" Level	0.9 V max
	Logic "1" Level	1.6 V min
	Load (Terminus to V _S -2V)	100 Ω
Enable / Disable Function		Pin 1: High or Open / Output enabled (Pins 4 & 5) Pin 1: Low / Output disabled (High impedance)
RMS Phase Jitter (12kHz ~ 20 MHz)		1 ps max

MECHANICAL SPECIFICATION



PART NUMBERING SYSTEM

TYPE	SERIES	VOLTAGE (V)	STABILITY (ppm)	TEMPERATURE RANGE (°C)	PULLABILITY (ppm)	SYMMETRY (%)	FREQUENCY (MHz)
VLF	5	3: 3.3	20: ±20 25: ±25 50: ±50 10: ±100	JZ: -10 ~+70 HZ: -20 ~+70 D3: -40 ~+85	50: ±50 100: ±100	blank: 40~ 60 T: 45~55	50.000 ~ 200.000

EXAMPLE: VLF5325-D3-100-T-155.520

Surface Mount VLF5 Series, LVDS Fundamental VCXO, 5.0 x 3.2 mm, 3.3 VDC Supply Voltage, ±25 ppm Stability from -40°C to +85°C, ±100 ppm Frequency Adjustment Range, Symmetry 45% to 55%, 155.520 MHz