

VOLTAGE CONTROLLED CRYSTAL OSCILLATORS HCMOS/TTL 5V



5 x 7 mm Surface Mount Commercial: 0° to 70°C 3 MHz to 45 MHz

GUARANTEED CAPTURE RANGE/ABSOLUTE PULL RANGE

Guaranteed Capture Range (GCR) and Absolute Pull Range (APR) are terms often used interchangeably. MF's Guaranteed Capture Range (GCR) is defined as the minimum guaranteed frequency deviation or "pull" (in ppm) around the nominal frequency, with all effects of temperature, variations in V_{DD} and load taken into account. This amount of absolute frequency deviation is available under all operating conditions for modulation or capturing other signals. No additional frequency capture allowances are necessary.

FEATURES

- Guaranteed Capture Range of ±75ppm or ±100ppm, depending on model
- · Excellent incremental and best-straight-line linearity
- Start-up time is less than 5ms
- · Each unit is ATE-tested to guarantee full compliance with all electrical specifications

TYPICAL APPLICATIONS

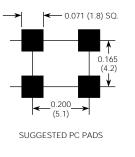
- · Phase locked loops and data acquisition projects, including:
- xDSL customer premise equipment
- Cable modems
- ATM/SONET/SDH

Description

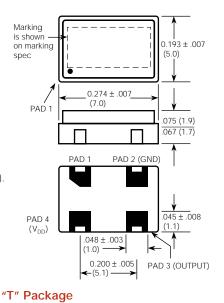
These SMD VCXOs generate a 5 volt HCMOS/TTL frequency output which is controlled ("pulled") by an input voltage. MF Electronics' VCXO specification defines not only the end-point frequency/ voltage parameters, but also the center voltage at which the nominal frequency is acheived.

CONNECTIONS

	T Package		
Pad 1.	Control Voltage		
Pad 2.	Ground		
	Output		
Pad 4.	+5V, V _{DD}		
	output +5V, V _{DD}		



Millimeters are shown in ().



230 200 160 - 30 -50 to 20 30 to 20 sec 100 sec 50 sec sec sec min min min Time (sec)

Recommended Reflow Soldering Profile

ΠF

VOLTAGE CONTROLLED CRYSTAL OSCILLATORS HCMOS/TTL 5V Surface Mount, 5V

Commercial: 0° to 70°C 3 MHz to 45 MHz

SURFACE MOUNT T2002, T2006 T2021, T2022, T2024 T2031, T2032 T2034

Center Frequency is Between Two Voltages with ±50 ppm stability

MODEL	Letter ID	Control Voltage (Volts)	Guaranteed Frequency Deviation (ppm)	Control Capture Range (ppm)	Center Voltage at Center Frequency	Frequency Stability (ppm)	
T2002	VA	0.3 to 4.0	± 75 min	± 75	1.3 to 2.3	50. max	
T2006	VB	0 to 5.0	± 100 min	± 100	-	50, mux	

Center Frequency is at 2.5V with ±50 ppm stability

MODEL	Letter ID	Control Voltage (Volts)	Guaranteed Frequency Deviation (ppm)	Control Capture Range (ppm)	Center Frequency Voltage (Volts)	Frequency Stability (ppm)
T2021	VC	0.5 to 4.5	± 75 to 150	± 75	2.5	± 30 typ
T2022	VD	0.5 to 4.5	± 100 to 200	± 100	2.5	± 50 typ
T2024	VE	0 to 5.0	± 100 to 250	± 100	2.5	± 00, max

Center Frequency is at 2.5V with ±25 ppm stability

	MODEL	Letter ID	Guaranteed Control Voltage (Volts)	Control Frequency Deviation (ppm)	Center Capture Range (ppm)	Frequency Voltage (Volts)	Frequency Stability (ppm)
	T2031	VF	0.5 to 4.5	± 75 to 150	± 75	2.5	± 20 typ
ſ	T2032	VG	0.5 to 4.5	± 100 to 200	± 100	2.5	± 25, max
	T2034	VH	0 to 5.0	± 100 to 250	± 100	2.5	1 20, max

T2002	±75 ppm, min. deviation when using 0 to 4.0V control-voltage
T2006	±100 ppm, min. deviation when using 0 to 5.0V rail-to-rail control-voltage
T2021	±75 ppm capture when using using 0.5 to 4.5V control-voltage and 2.5V center with ±50 ppm stability
T2022	±100 ppm capture when using using 0.5 to 4.5V control-voltage and 2.5V center with ±50 ppm stability
T2024	±100 ppm capture when using using 0 to 5.0V control-voltage and 2.5V center with ±50 ppm stability
T2031	±75 ppm capture when using using 0.5 to 4.5V control-voltage and 2.5V center with ±25 ppm stability
T2032	±100 ppm capture when using using 0.5 to 4.5V control-voltage and 2.5V center with ±25 ppm stability
T2034	±100 ppm capture when using using 0 to 5.0V control-voltage and 2.5V center with ±25 ppm stability

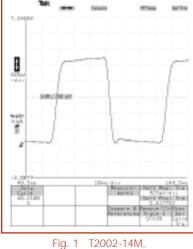
FREQUENCY STABILITY

DESCRIPTIONS

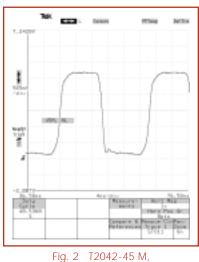
Frequency stability vs. Temperature (0 to 70°C) is typically better than ± 20 ppm. Since the deviation of each oscillator is tested and guaranteed over the whole operating temperature range, it is not necessary to make additional capture allowances. All oscillators will capture frequencies with the full minimum values of the deviation under all conditions.

QUALITY

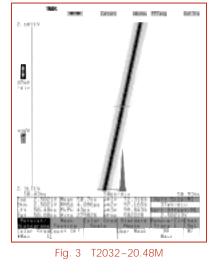
Each VCXO is computer-tested at three temperatures to guarantee full compliance to the specification.



g. 1 12002-14M with 50 pf load







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VOLTAGE CONTROLLED CRYSTAL OSCILLATORS HCMOS/TTL 5V Surface Mount, 5V Commercial: 0° to 70°C

3 MHz to 45 MHz

ELECTRICAL SPECIFICATIONS

Frequency Range	3 MHz to 45 M	Hz			
Frequency Stability	Includes calibra change of input vibration.			0 1	
		MIN	TYP	MAX	UNITS
Input Voltage		4.5	5.0	5.5	volts
Input Current, 3M to 8M	o 15 (10	
	@ 15 pf @ 50 pf			12 14	mA mA
	@ 15 pf @ 50 pf 4			18 22	mA mA
5.5 V _{DD}	@ 15 pf @ 50 pf			26 30	mA mA
Output Levels "0" Level, sinking "1" Level, sourci		V _{DD} 4		0.4	volts volts
Rise and Fall Times, HCMOS, from 2 HCMOS, from 2	0 to 80%, 15 pf			2.5 5.0	ns ns
Symmetry 10 TTL, @ 1.4 V, NL to 30 pf (HCI NL to 50 pf (HCI	MOS)	>	30 MHz	45/55 45/55 40/60	percent percent percent
Aging First year After first year			3 1		ppm ppm/yr
Input Impedance, Pad 1., Control V	Voltage	100	1000		Kohms
Control Voltage Ba	ndwidth	15	75		KHz

PAD 1 CONTROL VOLTAGE FET PROBE Y-OUTPUT 50 MHz SCOPE MA Power LOAD Supply 0.1 MF mmm To adapt Fet probe to receptacle To connect output to scope use use Tektronix Part #103-0164-00 use Tektronix Part #131-0258-00 (receptacle)

TEST CIRCUIT

ENVIRONMENTAL SPECIFICATIONS

Temperature

Operating	0° to 70°C
Storage	-55° to +125°C

Temperature Cycle – Not to exceed ±5 ppm change when exposed to 2 hours maximum at each temperature from 0 to 120°C, with 25°C reference

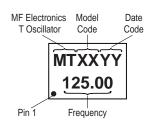
Shock - 1000 Gs, 0.35 ms, 1/2 sine wave, 3 shocks in each plane Vibration - 10-2000 Hz of .06" d.a. or 20 Gs, whichever is less Humidity - Resistant to 85° R.H. at 85°C

MECHANICAL, SPECIFICATIONS

Gross Leak - Each unit checked in 125°C fluorocarbon Fine Leak - Mass spectrometer leak rate less than 5 X 10 (-8) atoms, cc/sec of helium Pads - 60 microinch of gold over nickel Marking - Print is permanent Resistance to Solvents - MIL STD 202, Method 215

MARKING SPECIFICATION

The format for the marking is:



T2024

T2034

SURFACE MOUNT T2002, T2006 T2021, T2022,

T2031, T2032