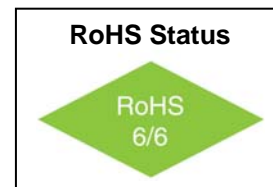


T5621 Series
T5721 Series
5x7 mm Surface Mount High Reliability
Tristate/Non-Tristate, 16kHz to 100MHz



Features

- Leadless chip carrier package is hermetically sealed for superior aging and field performance
- Crystal angle controlled to ± 1 minute for excellent temperature stability
- 168 hour Class B burn-in and extensive environmental testing for best performance in rugged field environments
- Start-up time <10 ms, typical
- Tristate option available
- Calculated MTBF is 3.8×10^6 hours at 125°C



Application

- Surface Mounted PCB projects requiring high reliability HCMOS clock waveforms

Description:

These high reliability oscillators provide HCMOS clock waveforms for applications subjected to the most stringent environmental conditions. They are mechanically robust and weigh less than 0.2 grams. This 5x7mm SMD package has a hermetic seal, thus ensuring the integrity of the part. Each oscillator is burned-in at 125°C for 168 hours, temperature cycled and centrifuged then fully tested in accordance with Table 1. Reliability tests are performed per Table 2.

Electrical Specifications

Parameter	Symbol	Condition	Min	Typ	Max	Unit	Note
Frequency Range	F		0.0016		100	MHz	
Frequency Stability	$\Delta F/F$	Vs. Operating Temperature	± 25		± 75	ppm	See Chart
		Aging 1 st Year After 1 st Year			± 3 ± 1		
Operating Temperature Range	T		-55		+125	°C	See Chart
Input Voltage	V _{cc}		4.5	5.0	5.5	V	
Input Current	I _{cc}			20	35	mA	
Waveform Symmetry		Measured at 50% V _{DD}		40/60	60/40	%	
Rise / Fall Time	T _r /T _f	<10MHz ≥10MHz		5 2	15 5	ns	0.8 to 2.4 volts
Output Level	“Zero” Level	Sinking 16mA			0.5	V	
	“One” Level	Sourcing 10mA	4.5				
Input Requirement for Pin.1		Output enable - pin 1 may float or 2.8V min(sourcing 400 uA) Output disable (Tristate) pin 1 requires 0.4V max (sinking 400 uA)					

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Environmental and Mechanical Conditions

Parameter	Conditions
Shock	MIL-STD 883, Method 2002, Test Condition B (1500 peak g, 0.5 ms duration, 1/2 sine wave, 5 shocks in 6 planes)
Vibration	MIL-STD 883, Method 2007, Test Condition A (20-2000Hz of 0.06" d.a. or 20 Gs, whichever is less)
Humidity	Resistant to 85° R.H. at 85°C
Leak	MIL-STD-883, Method 1014, Condition A and Condition C
Case	Hermetically sealed ceramic LCC
Pads	60 microinch of gold over nickel
Resistance to Solvents	Per MIL-STD-202, Method 215
Marking	Epoxy ink or laser engraved

FIXED OUTPUT	TRISTATE	Operating Temperature	Frequency Stability
Model	Model		
T5621	T5721	-55°C to +85°C	±25 ppm
T5622	T5722	-55°C to +85°C	±50 ppm
T5623	T5723	-55°C to +125°C	±75 ppm
T5624	T5724	-55°C to +125°C	±50 ppm

Table 1

Each unit undergoes screening for product level B class 2 oscillators by MIL-PRF-55310

Internal Visual	
Stabilization Bake	MIL-STD-883 Method 1008, COND. B
Temperature Cycling	MIL-STD-883 Method 1010, COND. B
Constant Acceleration	MIL-STD-883 Method 2001, COND. A
Fine Leak	MIL-STD-883 Method 1014, COND. A
Gross Leak	MIL-STD-883 Method 1014, COND. C
Burn-in	MIL-STD-883 Method 1015, COND. B (125°C for 160 hours with bias)
Electrical test at 25°C	
Current	Frequency at max V _{DD}
Rise Time	Frequency at min V _{DD}
Fall Time	"Zero" logic level
Duty Cycle	"One" logic level
Tristate	
Frequency at 25°C and frequency verification at temperature extremes	

Serialized test data on each unit available upon request for additional cost.

Thermal Characteristics

Thermal Resistance

From Junction to Case, R_{θjc} 16 °C/Watt

Surface Mount Application

These packages are designed for reflow soldering in accordance with recommended profiles. For hand-soldering, the temperature of the iron should not exceed 400°C for three seconds.

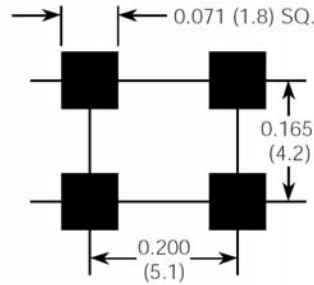
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Pin Assignments

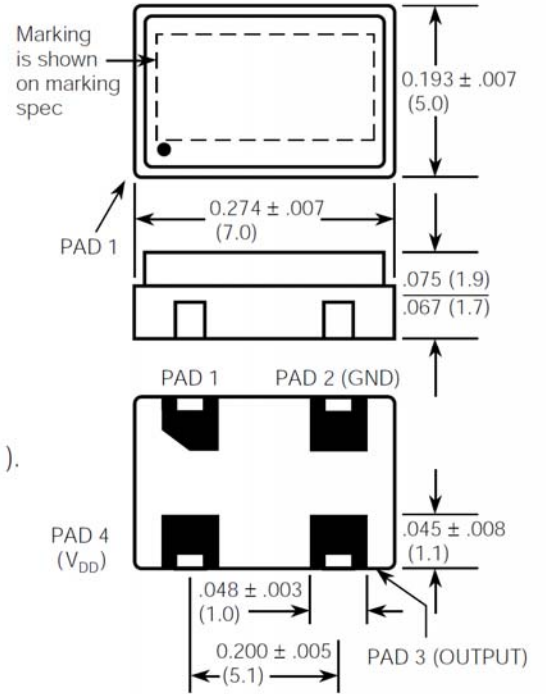
Pin #	T5621-5624	T5721-T5724
Pin 1	Not Connected	Tristate
Pin 2	Ground	Ground
Pin 3	Output	Output
Pin 4	+5.0V, V _{DD}	+5.0V, V _{DD}

Package

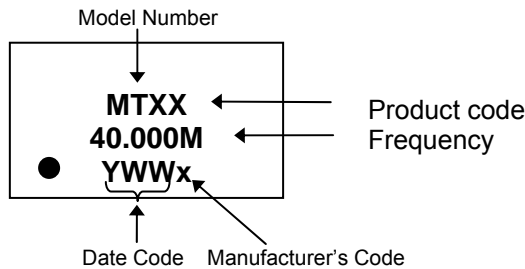


SUGGESTED PC PADS

Millimeters are shown in ().



Marking Specification



How to Order

