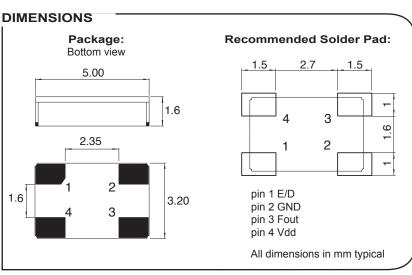


MCSO2EU ultra low consumption 32.768 kHz

T range up to 175°C





SMT Clock oscillator in ceramic package Fundamental quartz mode frequency High shock and vibration resistance Wide temperature range Low aging Ultra low internal MSL Very fast start-up Excellent solderability Swiss made quality Customer specification on request

DESCRIPTION:

This SMD oscillator in ceramic package has been specially designed for surface mount using infrared, vapor phase or epoxy techniques.

APPLICATIONS:

- Downhole and Well drilling equipments
- Avionics
- Airbone equipments
- Geothermal equipments
- Fire fighter equipments

The MCSO2's are supplied on trays (128 pcs / tray) For pick-and-place equipment, the parts are available in 12mm tapes with 250 parts min 1000 parts min

ELECTRICAL	
CHARACTERISTICS AT +	-25°C

Frequency stability

included 1000h at Tmax

Frequency stability Over temperature range C = -55 to +125°C (see ordering info) Including 2)*	ΔF/F	≤±100	ppm
Frequency stabilityOver temperature range E = -55 to +150°C(see ordering info)Including 2)*	ΔF/F	≤±150	
Frequency stability Over temperature range D = -55 to +175°C (see ordering info) Including 2)*	ΔF/F	≤±300	
Supply voltage ± 5% 1)*	Vdd	2.5 / 3.3	V
Input current	ldd	see table 1	
Output signal		HC-MOS compatible	
Symmetry at Vdd/2		40 / 60	%
Rise & fall time (load 15pf 20% to 80%)		25	ns
Level "0" & "1"		<0.4>Vdd-0.5	V
Start-up time	t	<5	ms
Load min / max		3/27	pF

TABLE 1: Idd (With load 10pF)

Frequency	F=32 kHz
W=Vdd = 2.5V	< 20µA
V=Vdd = 3.3V	< 20µA

STANDARD FREQUENCIES:

Frequency «kHz»		
32.768		
Other frequencies from up to 50 kHz on request		

ENVIRONMENTAL CHARACTERISTICS:

Storage temp. range-65 to +125°CVibration resistance10 to 2000Hz / 40gShocks resistance10000g / 0.3ms / ½ sine

TERMINATIONS AND PROCESSING:

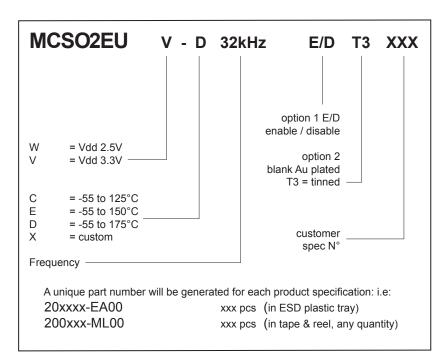
PRODUCT DESCRIPTION AND

ORDERING INFORMATION:

Reflow soldering	+260°C / 10s max
Package	Ceramic 5 x 3.2 x 1.6mm
Lids	Ceramic
Terminations option T3 on request	with tinned Ag/Cu/Zn
E/D option 1 on request Reaction time < 5mS	$\begin{array}{ccc} \mbox{Pin 1 open} \rightarrow \mbox{Pin 3 Clock} \\ \mbox{H} & \rightarrow & \mbox{Clock} \\ \mbox{L} & \rightarrow & \mbox{Low} \end{array}$

- No power E/D function (pin 1) before Vdd is setting on

- Disable option (consumption ≤5µA).



All specifications subject to change without notice.



CEOB2B晶振平台-全球最专业的晶振在线采购查询平台http://www.crystal95.com